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INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT

Date Submitted: October 13, 2006

(use as many sheets as necessary)

Sheet 1 of 1

## Complete if Known

Application Number	10/811,925
Filing Date	03/30/2004
First Named Inventor	Atsufumi SHIBAYAMA
Group Art Unit	2193
Examiner Name	Chaki Katakai
Attorney Docket Number	040405-0368

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## U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code* (if known)			
TI	C1	2003/0014471	A1	OHSAWA et al.	01-16-2003	

## UNPUBLISHED U.S. PATENT APPLICATION DOCUMENTS

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## FOREIGN PATENT DOCUMENTS

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		Office*	Number*	Kind Code* (if known)				
TI	C2	JP	2003-029984	A	NEC CORPORATION	01-31-2003		A
	C3	JP	2003-029985		NEC CORPORATION	01-31-2003		A
	C4	JP	2001-282549		NEC CORPORATION	10-12-2001		A
	C5	JP	10-078980		NEC CORPORATION	03-24-1998		A
TI	C6	JP	10-027108		NEC CORPORATION	01-27-1998		A

## NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.	T*
TI	C7	J. SAKAI et al., "Automatic parallel compiling techniques for control parallel architecture," 1998 Parallel Processing Symposium Japan, Information Processing Society, June 3, 1998, Vol. 98:7, pp. 383-399.	
TI	C8	J. TORII et al., "Proposal for the on-chip control parallel processor MUSCAT," Information Processing Society, June 15, 1998, Vol. 39:6, pp. 1622-1631.	

Examiner Signature

Date Considered

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